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| SECOND INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>(use as many sheets as necessary)</small> | | Complete if Known | |
| | | Application Number | 09/872,803 |
| | | Filing Date | September 28, 2000 |
| | | First Named Inventor | Jiren YUAN |
| | | Examiner Name | My Trang Ton |
| Sheet 1 of 1 | Attorney Docket Number | 026126-068 | |

| Examiner Initials | Document Number | Kind Code (if known) | Name of Patentee or Applicant of Cited Document | Issue/Publication Date (MM-DD-YYYY) |
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| Examiner Initials | Document Number | Kind Code (if known) | Country | Date of Publication (MM-DD-YYYY) | Translation Yes No |
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| Examiner Initials | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. |
|----------------------|---|
| MT | CARLEY, L. RICHARD, et al., "High-Speed Low-Power Integrating CMOS Sample-and-Hold Amplifier Architecture," Custom Integrated Circuits Conference, IEEE, 1995, pp 543-546. |
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| Examiner Signature |  | Date Considered | 04/12/04 |
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